

	FLOW 1	FLOW 2	FLOW 3	TC
CC1	128	32	0	160
CC2	64	32	8	96
CC3	128	64	8	200
CC4	64	0	0	64
CC5	0	0	8	8

ALLOWED SUBSET  
OF CCs

CAPACITY  
e.q. BIT RATE

FIG. 1

	FLOW 1	FLOW 2	FLOW 3	TC
CC1	128	64	8	200
CC2	128	32	0	160
CC3	64	32	8	96
CC4	64	0	0	64
CC5	0	0	8	8

ALLOWED SUBSET  
OF CCs

POINTER

FIG. 2

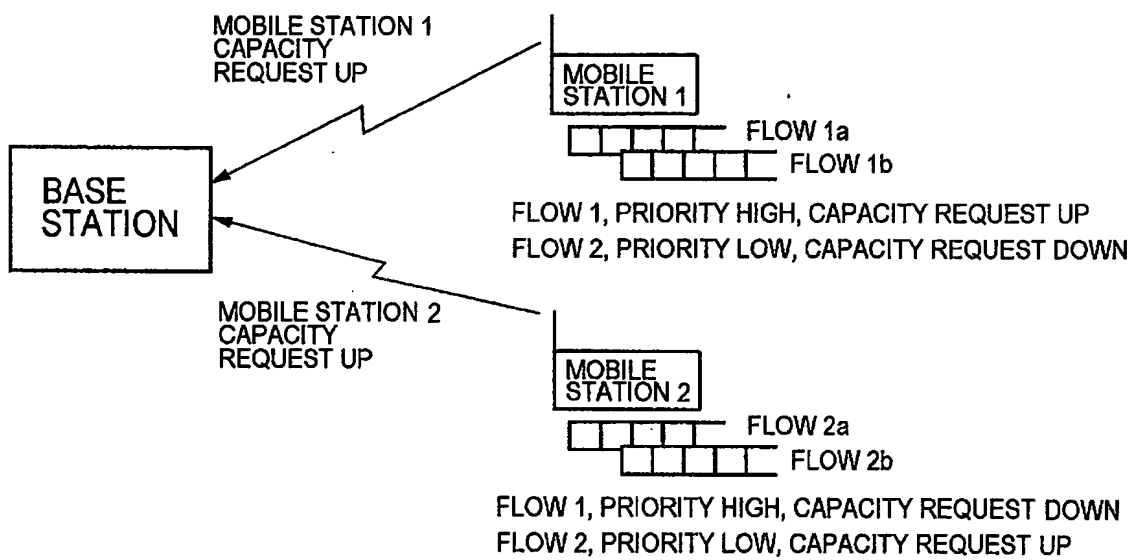


FIG. 3

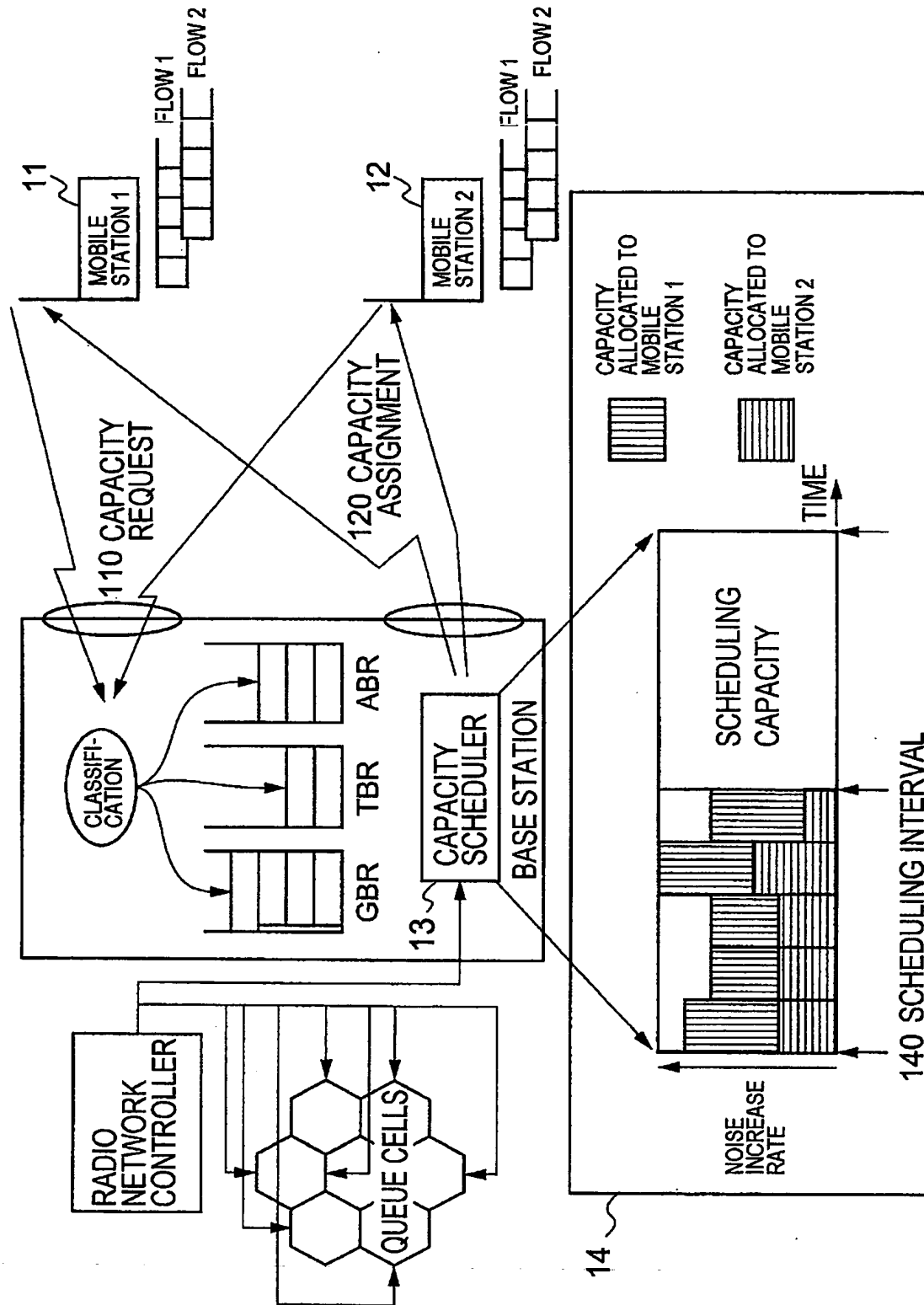


FIG. 4

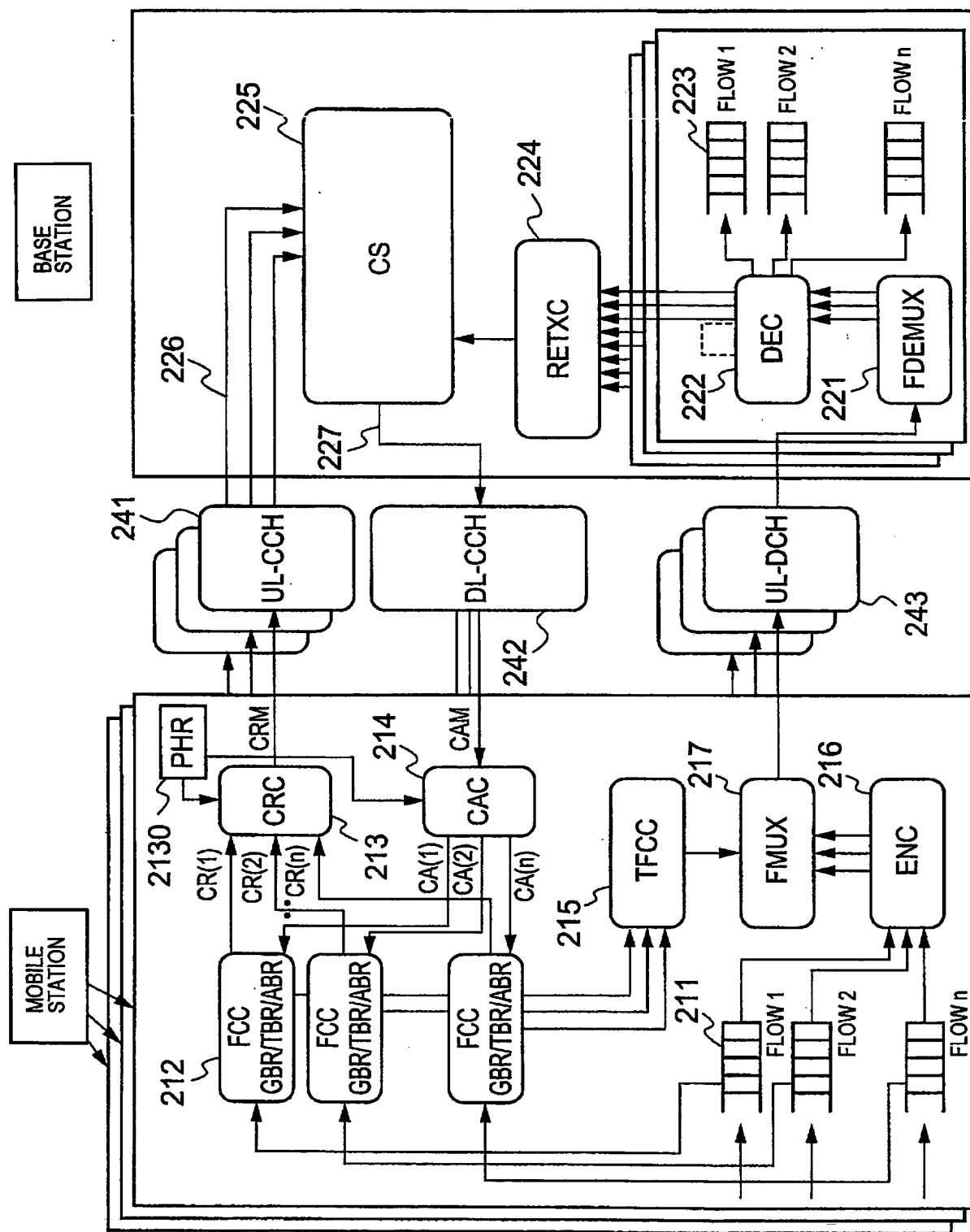


FIG. 5

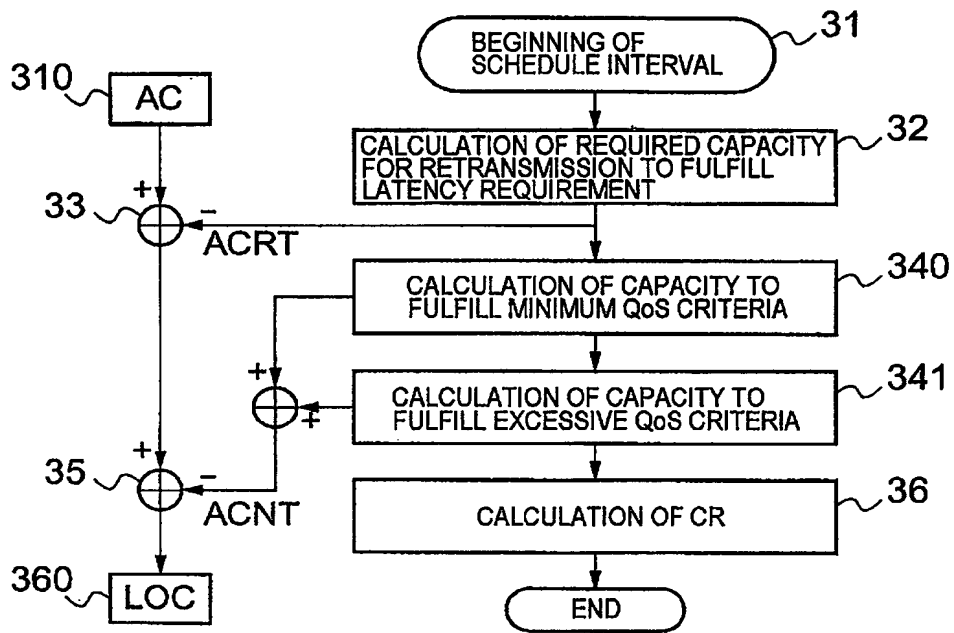


FIG. 6

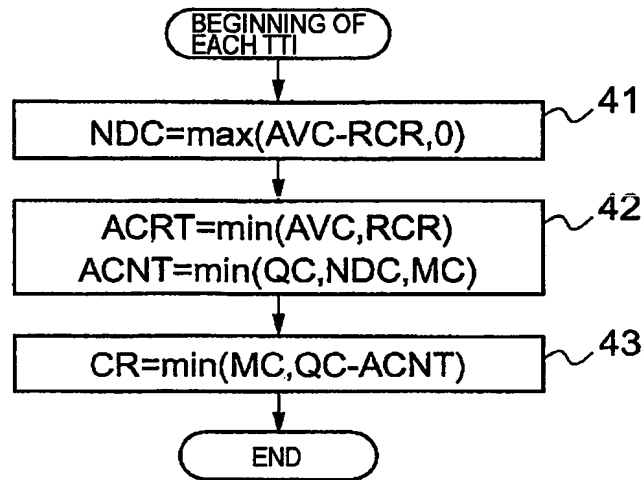


FIG. 7

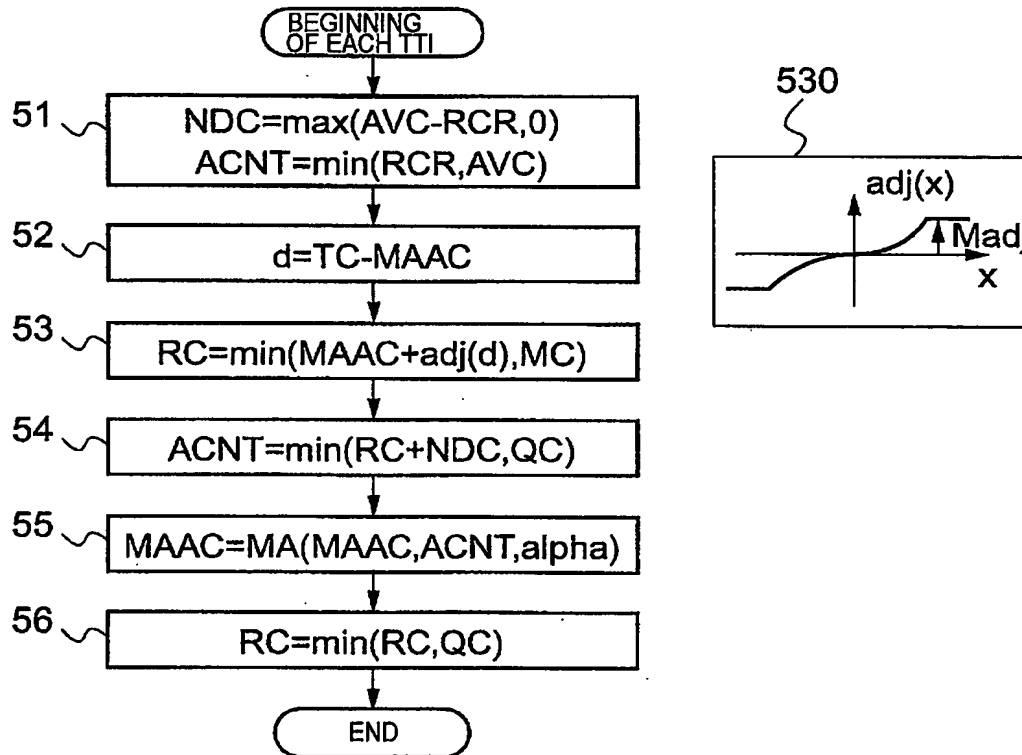


FIG. 8

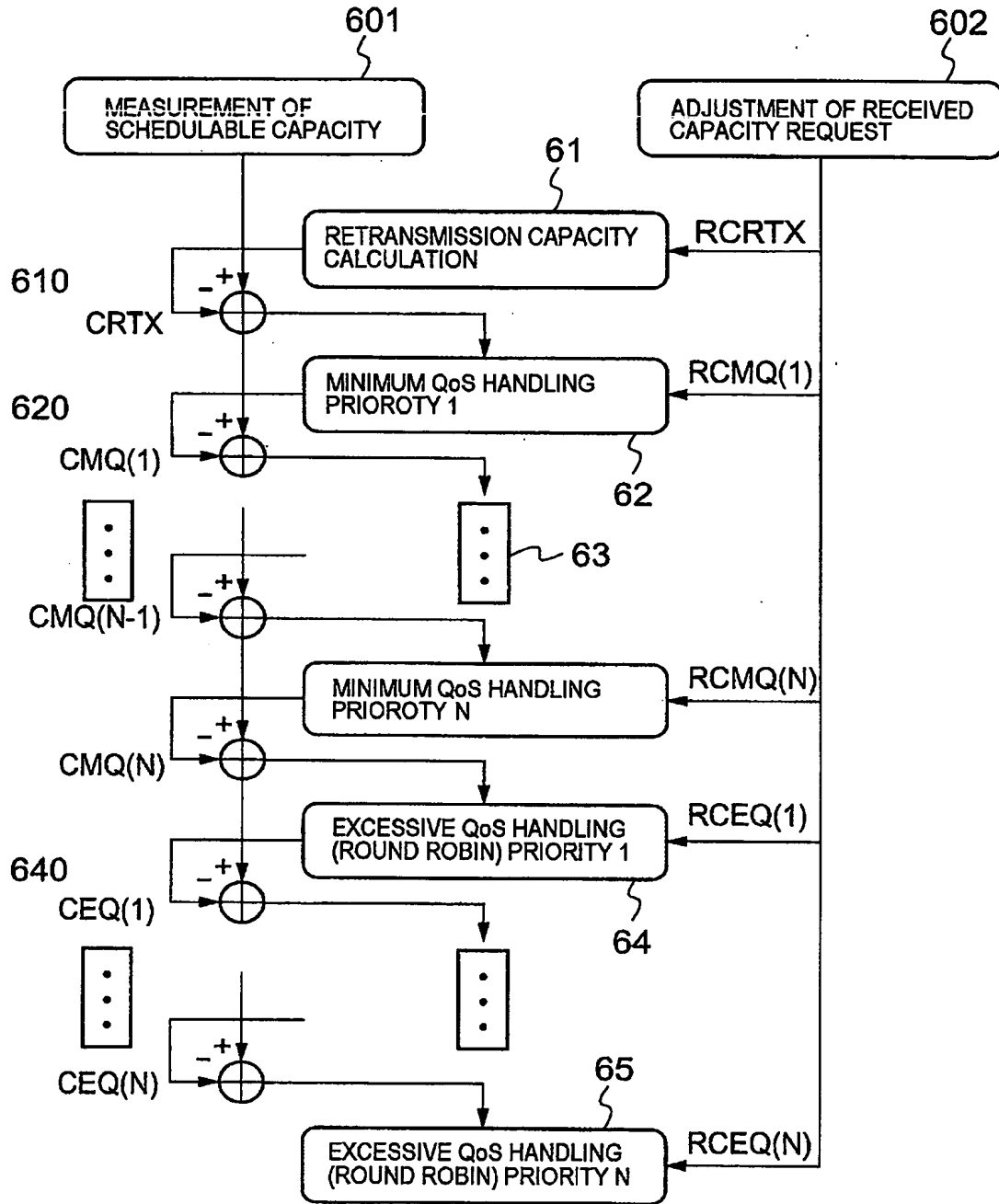


FIG. 9

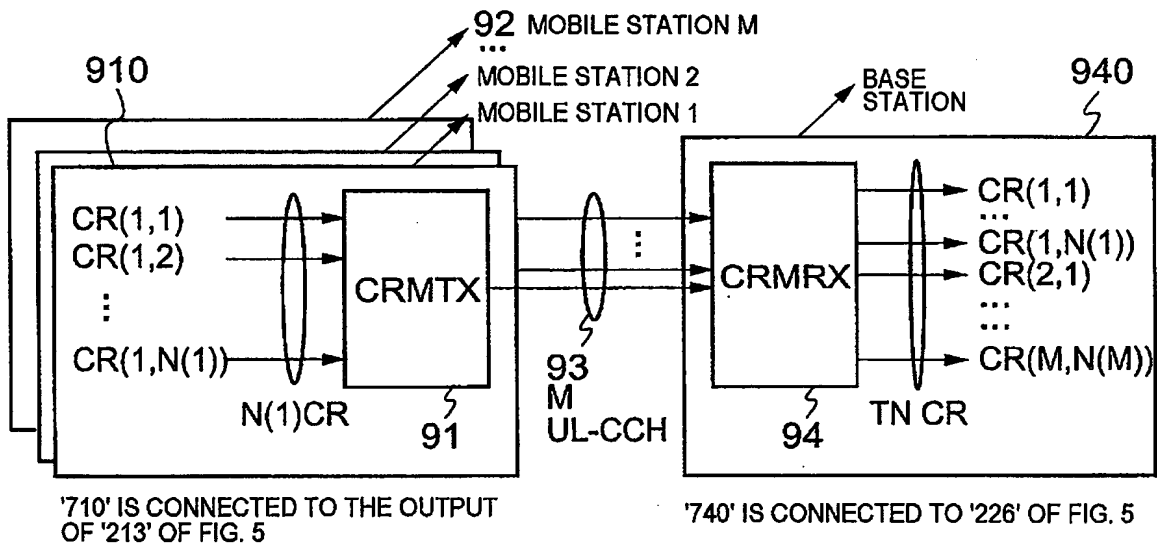


FIG. 10



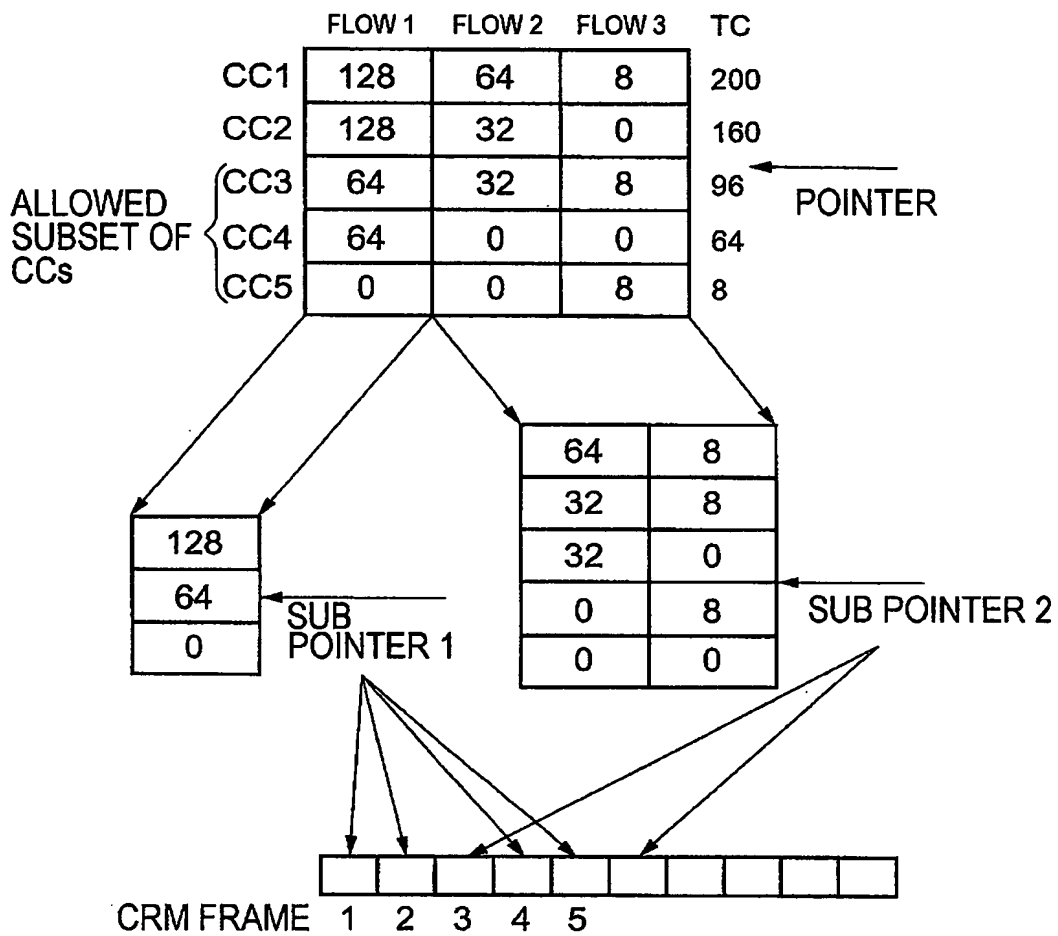


FIG. 11

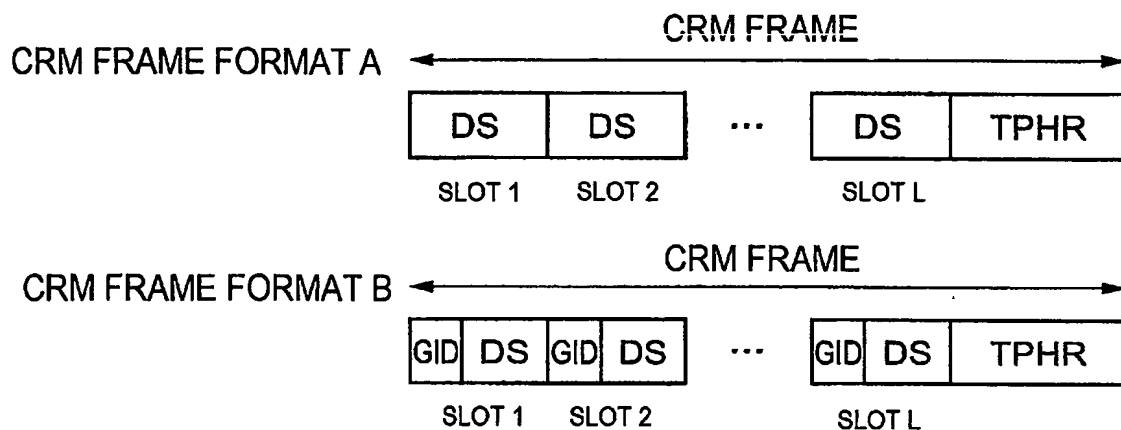


FIG. 12

FRAME NUMBER	0	1	2	3	4	5	6	7	c1
SLOT = 1	1	2	1	2	1	2	1	2	
SLOT = 2	3	3	3	4	3	3	3	4	

FRAME NUMBER	0	1	2	3	4	5	6	7	c2
SLOT = 1	1	1	1	4	3	2	1	3	
SLOT = 2	2	2	2	2	2	3	3	4	

FIG. 13

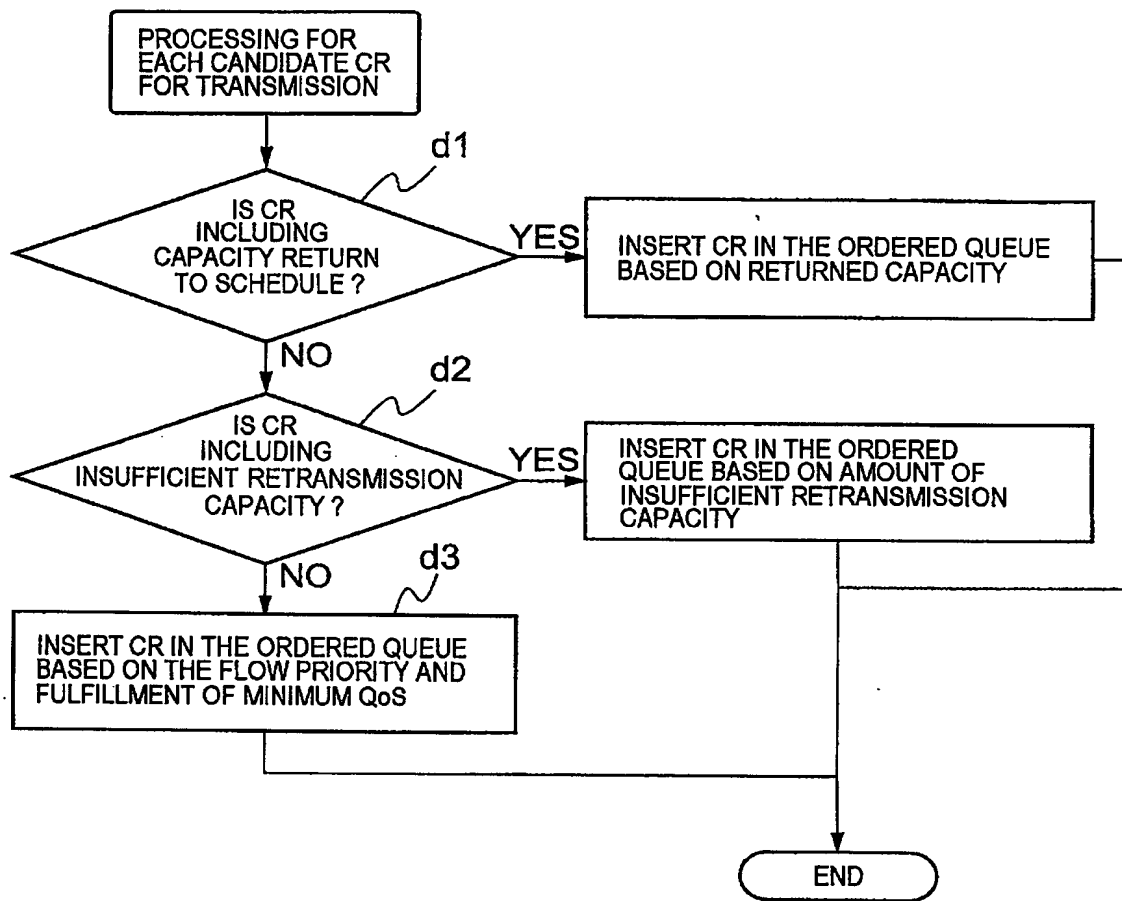


FIG. 14

